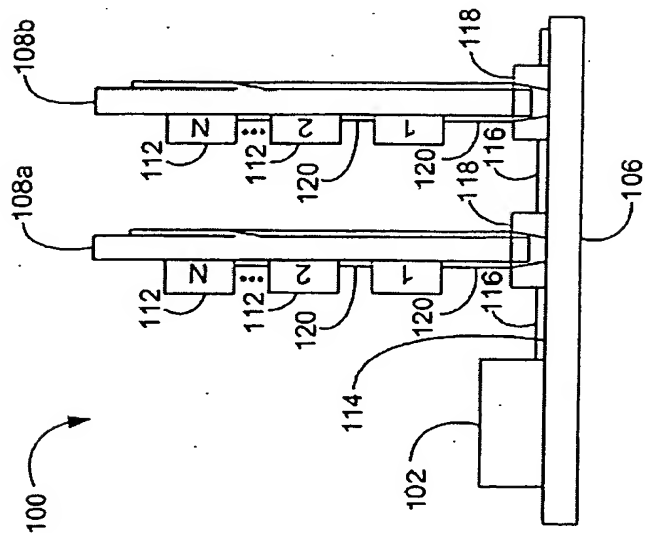
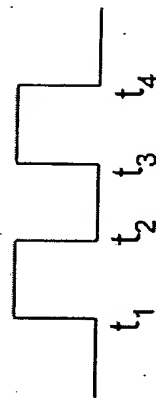
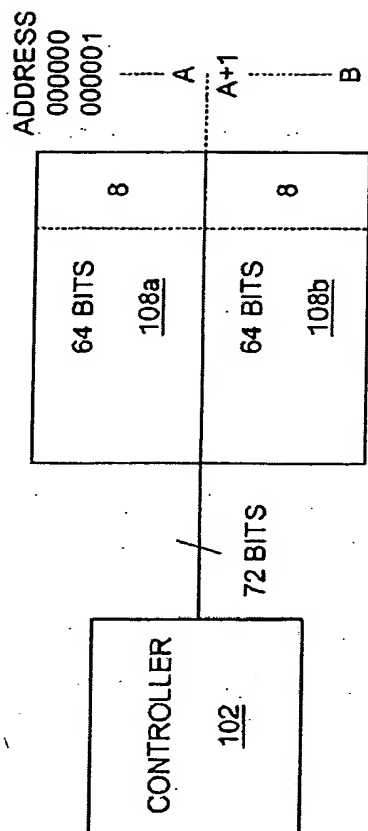


1/7



2/7

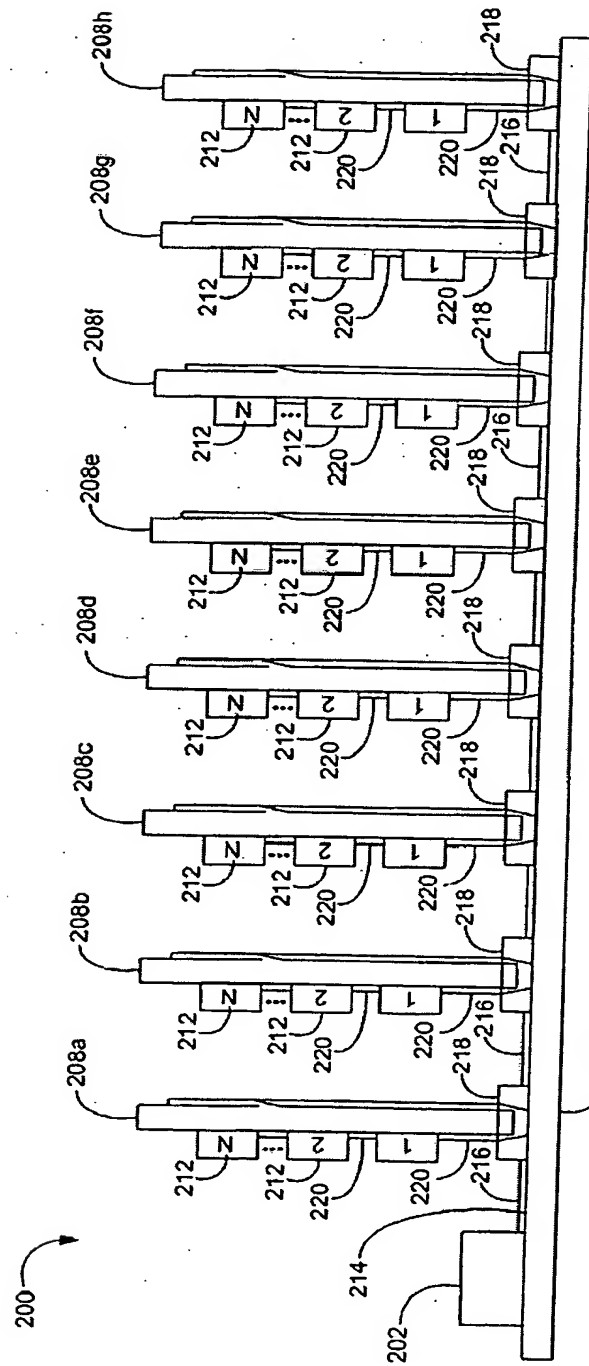


FIG. 2A (Background Art)

3/7

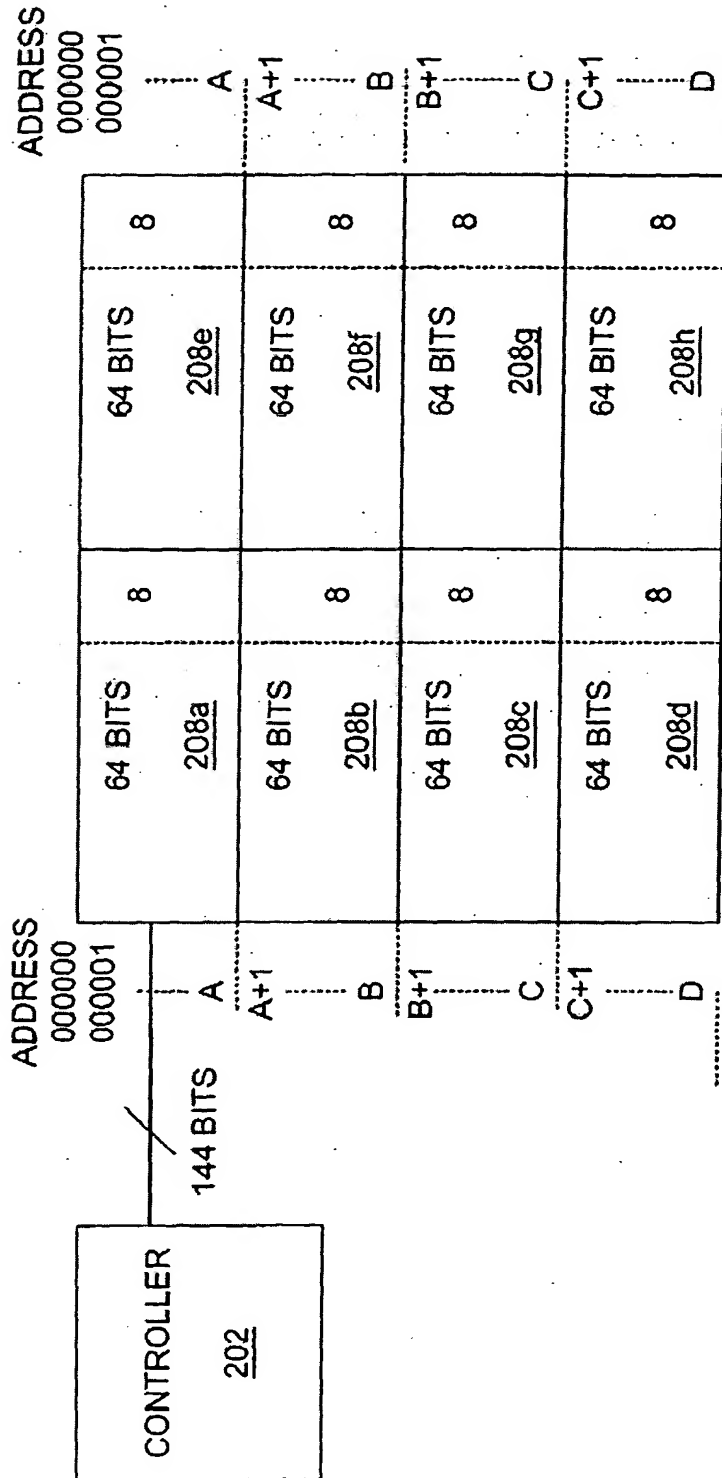


FIG. 2B (Background Art)

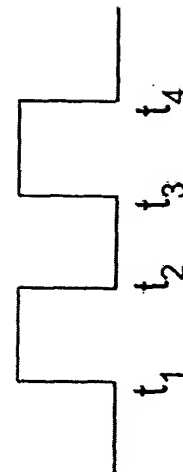


FIG. 2C (Background Art)

4/7

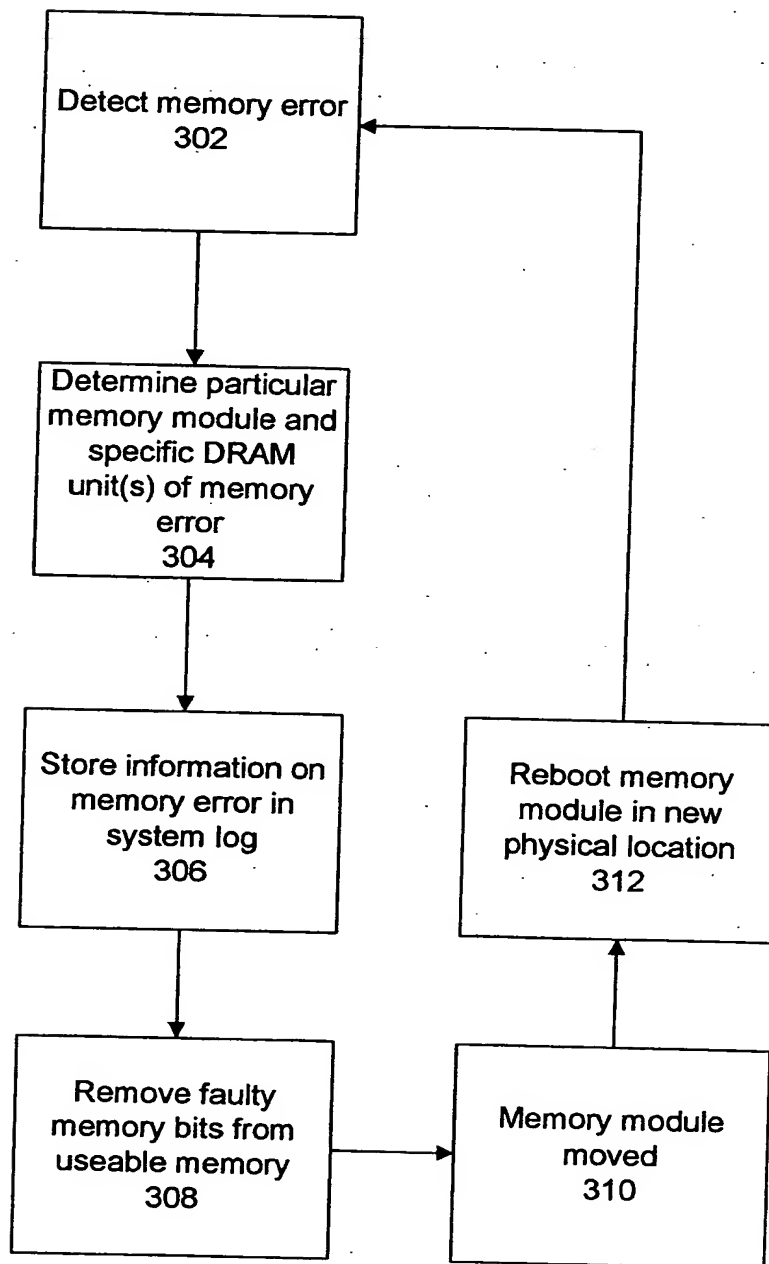


FIG. 3  
(Background Art)

5/7

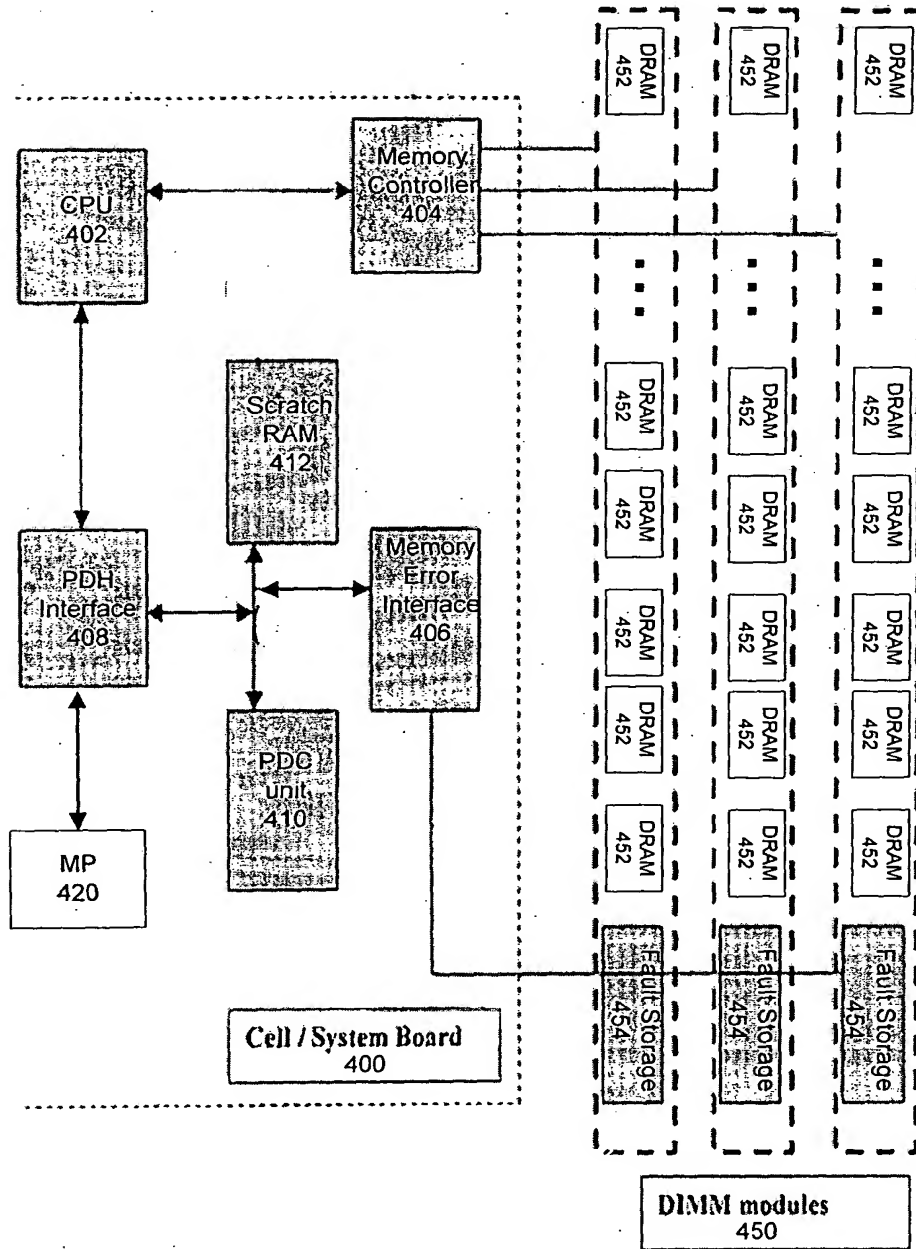


FIG. 4

6/7

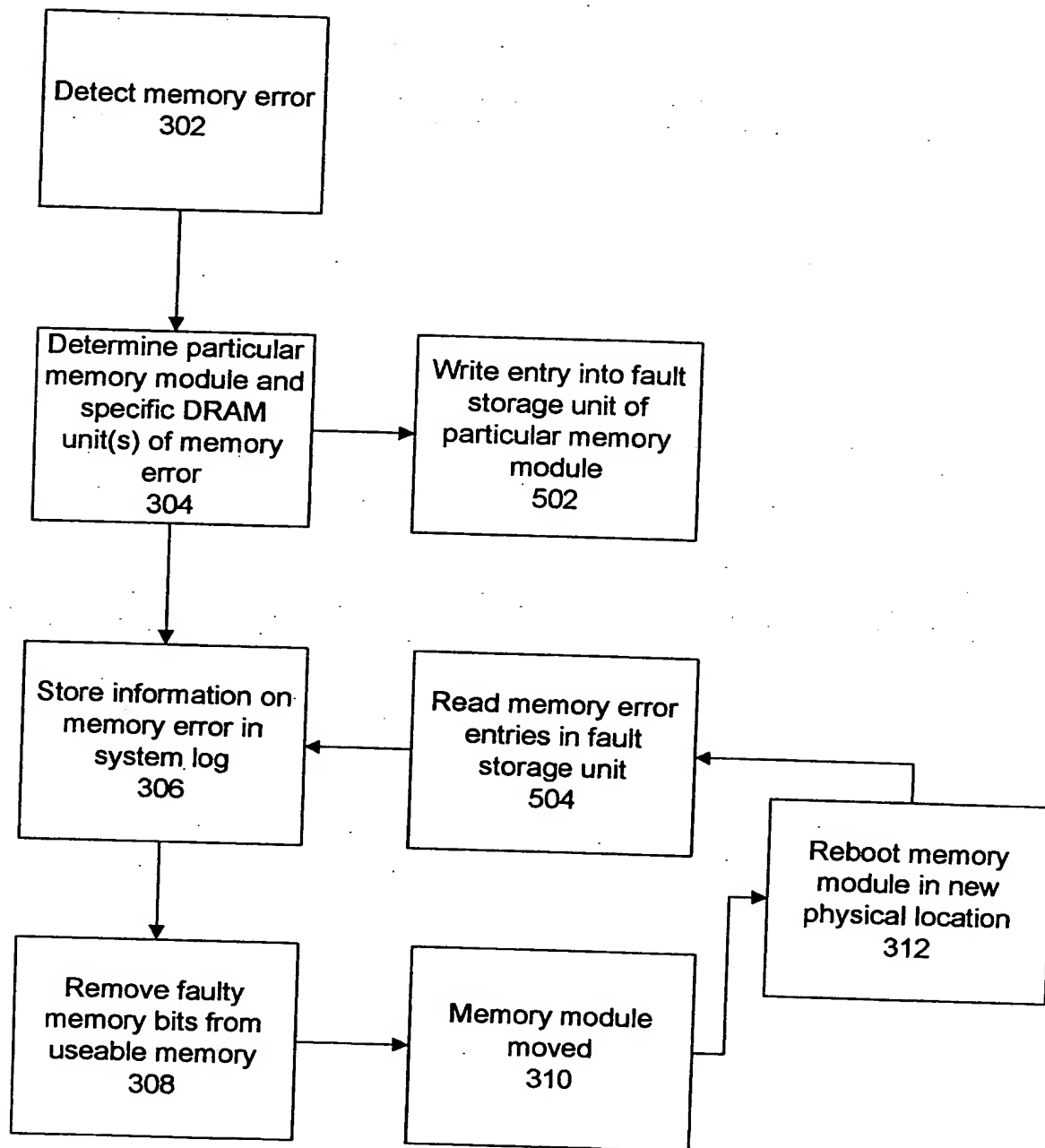


FIG. 5

7/7

DRAM # 602	Start bit of error 604	End bit of error 606

FIG. 6